

## EAST Search History

| Ref # | Hits  | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|-------|--|---|------------------|---------|------------------|
| L2    | 12030 | processing core or multiprocessor core or multi-core or processor core | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:44 |
| L3    | 3     | internal memory same test routine\$1 same integrated circuit           | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:48 |
| L4    | 8386  | "BIST" or built-in test or built-in self test or in-circuit test       | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:46 |
| L5    | 26    | interface same test result\$1 same external device                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:47 |
| L6    | 408   | I2 and I4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:47 |
| L8    | 1     | I3 and I6  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:47 |
| L9    | 101   | execut\$3 and (test routine\$1 same test result\$1)                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:48 |
| L10   | 1     | I6 and I9  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ              | ON      | 2006/05/26 20:48 |

## EAST Search History

|     |         |  |   |     |    |                  |
|-----|---------|--|---|-----|----|------------------|
| L11 | 119     | test routine\$1 same integrated circuit                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:49 |
| L12 | 14      | l11 and l6   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:54 |
| L13 | 1       | l12 and l9   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:49 |
| L14 | 1505    | microprocessor core\$1                                     | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:55 |
| L15 | 30692   | test pattern\$1 or test routine\$1                         | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:55 |
| L16 | 1409290 | microprocessor core\$1 same l15 same pass adj fail or fall | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:55 |
| L17 | 11      | l16 and l12  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:57 |
| L18 | 4508    | 714/30 or 714/733 or 714/734 or 714/724                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:57 |

## EAST Search History

|     |    |                             |   |     |    |                  |
|-----|----|-----------------------------|---|-----|----|------------------|
| L20 | 14 | I12 and (I14 or I15 or I16) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:58 |
| L21 | 3  | I20 and I18                 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | ADJ | ON | 2006/05/26 20:58 |